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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): D.E. Parson et al.
Case: 3-2-1-4
Serial No.:   To Be Assigned
Filing Date: May 30, 2000
Title: Control Method and Apparatus for Testing of Multiple
               Processor Integrated Circuits and Other Digital Systems
Group: To Be Assigned
Examiner: To Be Assigned

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed.

1. L. Goudge, "Debugging Embedded Systems," ARM White Paper, www.arm.com, 6 pages, 1998.

2. ScanProgrammer Product Information, ASSET InterTech, www.asset-intertech.com, 37 pages, March 2000.

3. Advanced Tutorial: Using the MVP Multiprocessing Features, Chapter 3, www.ti.com, pp. 3-1 through 3-17, undated.

4. Using the Parallel Debug Manager, Chapter 11, www.ti.com, pp. 11-1 through 11-21, undated.

5. BDM/JTAG Debug Interfaces, http://www.abatronag.ch, 6 pages, February 2000.

6. Multi-ICE™ Interface Unit, wysiwyg://4/http://www.arm.com, pp. 1-4, 2000.

7. Summit-ICE PCI Emulator, http://www.wmdsp.com, 1 page, 1998.

8. Mountain-ICE/WS JTAG Emulator-SBus, http://www.wmdsp.com, pp. 1-2, 1998.

9. Mountain-ICE JTAG Emulator-PC-ISA, http://www.wmdsp.com, pp. 1-2, 1998.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

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